Silicon Epitaxial Planar Diode for AM tuner

HITACHI

ADE-208-470A (Z) Rev. 1 Sept. 1, 1998

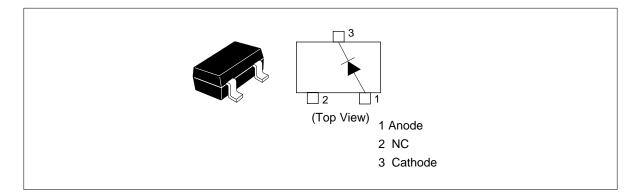
Features

- High capacitance ratio. (n =16.0 min)
- High figure of merit. (Q = 200 min)
- To be usable at low voltage.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HVM100	T12	MPAK

Outline



Absolute Maximum Ratings (Ta = 25° C)

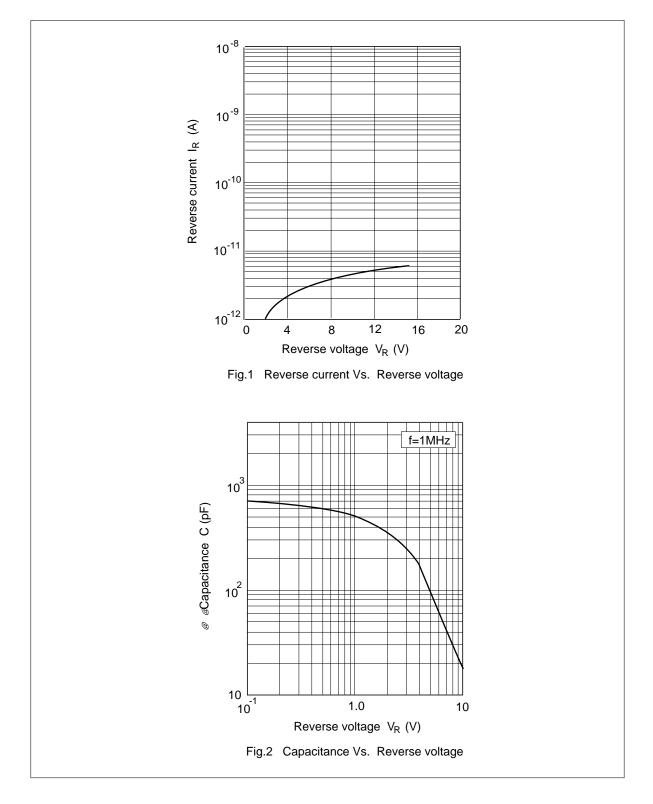
Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Тј	125	°C
Storage temperature	Tstg	-45 to +125	°C

Electrical Characteristics (Ta = 25°C)

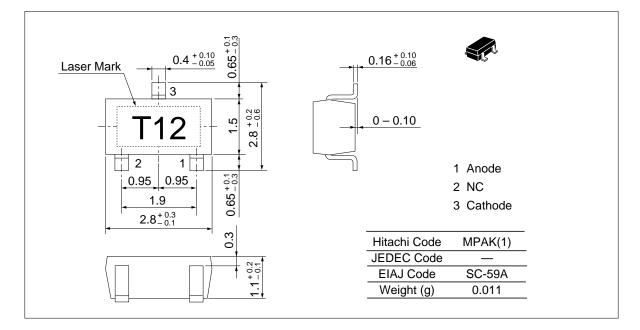
ltem	Symbol	Min	Тур	Мах	Unit	Test Condition
Reverse voltage	V _R	15	—	—	V	$I_R = 10\mu A$
Reverse current	I _R			100	nA	V _R =9V
Capacitance	C ₁	421.5	_	524.6	pF	$V_{R} = 1V, f = 1 MHz$
	C ₅	73.2	_	121.4	-	$V_R = 5V, f = 1 MHz$
	C ₈	20.4		28.2	_	$V_{R} = 8V, f = 1 MHz$
Capacitance ratio	n	16	_			C ₁ / C ₈
Figure of merit	Q	200				C = 450pF, f = 1 MHz
ESD-Capability ^{*1}	_	80	—	_	V	C=200pF , Both forward and reverse direction 1 pulse.

Notes 1. Failure criterion ; $IR \ge 100\mu A$ at VR =9 V

Main Characteristic



Package Dimensions (Unit : mm)



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